

CADENCE KOREA ES SCHEDULE - 1st half of 2023

11/30/2022

Course Name	Course ID	Length	S_Date	E_Date	Location	Price
Custom IC Design						
Virtuoso ADE Explorer & Assembler	86253	2	1/31/2023	2/1/2023	Cadence Korea	\$296
Virtuoso Layout Design Basics	84460	2	3/9/2023	3/10/2023	Cadence Korea	\$296
Mixed Signal Simulation Using Spectre AMS Designer (AVUM)	NA	2	3/23/2023	3/24/2023	IDEC (대전Kaist)	
Spectre Simulator Fundamentals	86210	2	3/27/2023	3/28/2023	Cadence Korea	\$296
SKILL Language Programming (Basic)	86322	2	4/6/2023	4/7/2023	Cadence Korea	\$252
Mixed Signal Simulation Using Spectre AMS Designer (AVUM)	86241	2	4/25/2023	4/26/2023	Cadence Korea	\$296
Virtuoso Layout Pro (Virtuoso_XL & GXL)	85092	2	5/11/2023	5/12/2023	Cadence Korea	\$296
Virtuoso ADE Explorer & Assembler	86253	2	6/1/2023	6/2/2023	Cadence Korea	\$296
SKILL Language Programming (Intermediate)	83018	3	6/13/2023	6/15/2023	Cadence Korea	\$378
Digital IC Design						
Genus Synthesis Solution with Stylus Common UI (English Course)	86220	2	1/16/2023	1/17/2023	Cadence Korea	\$296
Innovus Implementation System (Block)	86141	3	2/8/2023	2/10/2023	Cadence Korea	\$444
Innovus Implementation System (Hierarchical)	86142	2	2/15/2023	2/16/2023	Cadence Korea	\$296
Tempus Signoff Timing Analysis and Closure	82147	2	2/20/2023	2/21/2023	Cadence Korea	\$296
Conformal Equivalence Check & Conformal ECO	86133	3	4/12/2023	4/14/2023	Cadence Korea	\$444
SystemC Synthesis with Stratus HLS	86170	2	5/22/2023	5/23/2023	Cadence Korea	\$296
Innovus Implementation System (Block)	NA	2	6/14/2023	6/15/2023	IDEC (대전Kaist)	
Conformal Low Power Verification Using IEEE 1801	86284	2	6/21/2023	6/22/2023	Cadence Korea	\$296
Functional Verification						
Xcelium Simulator	86218	2	3/7/2023	3/8/2023	Cadence Korea	\$296
Xcelium Integrated Coverage	86225	2	4/4/2023	4/5/2023	Cadence Korea	\$296
Xcelium Simulator	86218	2	6/8/2023	6/9/2023	Cadence Korea	\$296
Silicon-Package-Board Co-Design						
Allegro Package Designer Plus	86263	3	1/18/2023	1/20/2023	Cadence Korea	\$444
TopXplorer SystemSI for Parallel Bus and Serial Link Analysis	86278	1	1/30/2023	1/30/2023	Cadence Korea	\$148
Allegro PCB Librarian	86022	1	2/2/2023	2/2/2023	Cadence Korea	\$148
Allegro Design Entry HDL Front-to-Back Flow	86015	2	2/13/2023	2/14/2023	Cadence Korea	\$296
Allegro PCB Editor Basic Techniques	86097	3	2/22/2023	2/24/2023	Cadence Korea	\$444
Model Generation and Analysis using PowerSI, Broadband SPICE	86137	1	2/28/2022	2/28/2022	Cadence Korea	\$148
Clarity 3D Solver	86269	1	3/6/2023	3/6/2023	Cadence Korea	\$148
Sigrity PowerDC and OptimizePI	85084	1	3/15/2023	3/15/2023	Cadence Korea	\$148
AWR Software Basic (Microwave Office for RF Designers)	86282	2	3/16/2023	3/17/2023	Cadence Korea	\$296
Allegro PCB Editor SKILL Programming Language	86099	3	3/29/2023	3/31/2023	Cadence Korea	\$378
Allegro Package Designer Plus	86263	3	4/18/2023	4/20/2023	Cadence Korea	\$444
TopXplorer SystemSI for Parallel Bus and Serial Link Analysis	86278	1	4/27/2023	4/27/2023	Cadence Korea	\$148
Allegro PCB Librarian	86022	1	5/10/2023	5/10/2023	Cadence Korea	\$148
Model Generation and Analysis using PowerSI, Broadband SPICE	86137	1	5/16/2022	5/16/2022	Cadence Korea	\$148
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Allegro Design Entry HDL Front-to-Back Flow	86015	2	5/24/2023	5/25/2023	Cadence Korea	\$296
Allegro PCB Editor Basic Techniques	86097	3	5/29/2023	5/31/2023	Cadence Korea	\$444
Sigrity PowerDC and OptimizePI	85084	1	6/20/2023	6/20/2023	Cadence Korea	\$148

* 상기 일정은 내부사정으로 인해 변경 될 수도 있습니다.

* Course ID를 클릭하시면 과정에 대한 내용 확인 및 등록 가능합니다.
- 웹사이트 내 length는 본사 기준으로 기재 되어 있습니다. 상단 표에 있는 length를 참조해 주시기 바랍니다.

* 홈페이지에서 등록시 입력 양식을 "국문"으로 기재해 주시기 바랍니다.
- 개인메일로는 신청 불가하며, 회사(혹은 소속 확인이 가능한) 메일로만 신청 가능합니다.

* 케이던스 코리아 교육 웹사이트:

https://www.cadence.com/content/cadence-www/global/ko_KR/home/training/all-courses.html

* Email: training_korea@cadence.com / Tel: 031-728-3036

* 교육마감: 교육시작 7~14일 전 (등록 상황에 따라 상이함)

* 신청하신 순서대로 순차적으로 등록처리 됩니다.

* 정원이 미달 될 경우 (5명 미만) 폐강처리 됩니다.

* IDEC 교육은 해당 기관 주관으로 진행되는 외부교육 입니다.

- 장소: IDEC (대전 Kaist) / 등록문의: ykim@idec.or.kr