cadence®

UMS PDKs

For use with Cadence AWR Design Environment platform

The Cadence® AWR Design Environment® platform is used by leading manufacturers to accelerate product development of high-frequency electronics. With support for multiple United Monolithic Semiconductors (UMS) process design kits (PDKs), AWR® software delivers a complete design environment for GaAs and GaN MMIC development. The UMS PDKs (refer to table below) work seamlessly with the latest release of AWR software. Further information is available at the UMS site at www.ums-gaas.com/foundry/design-kits.

Features

‣ Symbols and schematics

‣ Scalable models with RF accuracy

‣ Process corner statistical simulation

‣ Fully scalable layout PCells

‣ Advanced layout utilities

‣ Predefined substrate definitions

‣ Stackups for accurate electromagnetic (EM) analysis

UMS Process	PDK Description
GH25	0.25um GaN HEMT
PH10	0.1um Very Low-Noise High Gain GaAs pHEMT
PH15	0.15um Very Low-Noise GaAs pHEMT
PH25	0.25um Low-Noise GaAs pHEMT
PPH15X-20	0.15um High-Power GaAs pHEMT
PPH25	0.25um High-Power GaAs pHEMT
PPH25X	0.25um High-Power GaAs pHEMT
BES	GaAs Schottky Diode Technology

Cadence is a pivotal leader in electronic design and computational expertise, using its Intelligent System Design strategy to turn design concepts into reality. Cadence customers are the world's most creative and innovative companies, delivering extraordinary electronic products from chips to boards to systems for the most dynamic market applications. **www.cadence.com**